

HDTV Bit Error Rate Tester

This HDTV bit error rate (BER) tester features an LCD display for real-time visual monitoring of MPEG stream errors. It is designed for DTV broadcast transmission systems and includes a 23-order pseudo-random code test function.



Overview

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This HDTV Bit Error Rate Tester is a high-precision instrument engineered for comprehensive signal quality analysis in broadcasting, cable, and satellite applications. It supports both HDTV and SDTV standards, offering versatile testing capabilities through pseudo-random code sequences and MPEG2-compliant data packet structures. With flexible clock rate settings and multiple interface options, it provides engineers with a reliable solution for diagnosing and troubleshooting complex transmission systems.

Interface Specifications

Output Interfaces

- DVB ASI (BNC socket)
- SPI (D-25 Block)

Input Interfaces

- DVB ASI (BNC socket)
- SPI (D-25 Block)

Data Format

Supported Packet Formats

Format Type	Packet Size (Bytes)
Parallel Data	188 / 204 / 208
Serial Data	188

Clock Specifications

Internal Clock Rate

8 Mbps

Min Rate

80 Mbps

Max Rate

External Clock Specifications

Interface	Clock Type	Impedance
ASI	Bit clock	50 Ohm
SPI	Byte clock (Bit rate / 8)	50 Ohm

Internal Clock Accuracy Options

± 20 ppm, ± 10 ppm, ± 3 ppm, ± 1 ppm